

**P.B.SIDDHARTHA COLLEGE OF ARTS AND SCIENCE**

**DEPARTMENT OF ELECTRONICS**

**DEPARTMENTAL ACTIVITIES FOR YEAR 2018-19**

S.NO	DATE	ACTIVITY PERFORMED
1	18 to 22-6-18	Sri D.Srinivasreddy Attended five days workshop on IOT at NIT Warangal.
2	23-06-18	Sri K.S.V.Sambasivarao, Attend B.O.S meeting in Department of Electronics at Sri.Y.N.College, Narsapur,W.G.dst
3	16-07-18	Students of <b>II B.SC (M.E.Cs)</b> presented PPT's to I & III B.SC in seminar hall
4	9-8-2018	Guest lecturer in " <b>CMOS VLSI DESIGN</b> " by Dr.K.Hari kishore ,K.L.University, Vaddreswaram.
5	23-8-18	Students of II B.Sc(M.E.Cs) conducted <b>MINI PROJECT EXPO</b> to final and first year students.
6	30/11/18	Students of III B.Sc (M.E.Cs)& II B.Sc(M.E.Cs) Participated in 39 <sup>TH</sup> STATE LEVEL COMPETITION conducted by <b>K.B.N degree college</b> .
7	4/12/18	Students of II B.Sc(M.E.Cs) Participated IN CHEMP'S-2018 COMPETITION conducted by <b>ADITYA DEGREE COLLEGE</b> and WON second PRIZE in cultural.
8	4/12/18	Guest lecturer in " <b>VIRTUAL INSTRUMENTATION</b> " by K.UPENDRA CHOWDARY, R.V.R & JC ENGINEERING COLLEGE.
9	5,6,7/12/18	Students of III B.Sc (M.E.Cs)& II B.Sc(M.E.Cs) Participated in 58 <sup>th</sup> Gora science expo and won first prize.
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K.S.V.SAMBASIVARAO,

HEAD, DEPARTMENT OF ELECTRONICS

## **POWER POINT PRESENTATION:**

Students of **II B.SC (M.E.Cs)** presented PPT's to I & III B.SC in seminar hall on 16-07-18.

- The students of second year presented PPT'S on modern technology with the base of electronics and this PPT'S.
- These PPT'S can develop communication skills for their better presentation.
- They can also explains more innovative PPT'S.





### GUEST LECTURE

Guest lecturer in “**CMOS VLSI DESIGN**” by Dr.K.Hari kishore ,K.L.University,Vaddreswaram on 9-8-2018

- A guest lecture organized by department of Electronics on VLSI DESIGN which makes the students of Electronics to understand how an IC developed on VLSI.
- He also explains the utilization of software on VLSI(**DSCH TOOL**)
- Designers can create logic circuits for interfacing with these controllers (8051,PIC16F84) and verify software programs using DSCH.
- He designed CMOS NAND and NOR gates by using p-channel and N-channel MOS FET and p-channel MOSFET is **ON** for logic 0 (Negative gate voltage ) ;N-channel MOS FET is ON for logic 1 (Positive gate voltage ) and fabrication process of entire circuit on board and its simulation with output had been shown to the students.





### **MINI PROJECTS EXPO:**

Students of II B.Sc(M.E.Cs) conducted mini project expo to final and first year students on 23-8-2018.

- Students are progressively taking up mini projects to build up their skills
- By doing mini project they will gain practical knowledge inspite of theory concepts
- They can also show their capability by doing such innovative projects.
- These Experiences definitely help in designing major project as well as higher education.



### **GUEST LECTURE:**

Guest lecturer in “**VIRTUAL INSTRUMENTATION**” by K.UPENDRA CHOWDARY on 4-12-2018

- A guest lecture organized by department of Electronics on Virtual instrumentation which makes the students of Electronics to understand how an Instrument can utilize by using software.
- He also explains the utilization of software by using C programmes and array and he also explains about half adder and full adder changing the logic levels using front panel and in block diagram he drawn the circuit and shown how the current flows in particular direction.

- In order to do a practical in LAB we require function generator, cathode ray oscilloscope, block diagram, Bread board and connecting wires to do experiment while in this virtual lab view and we can easily get these instruments in built software and stimulate and easily get the result.
- The primary difference between hardware components and virtual instruments is additional having software for simulation and it is for fast real time processing.



**Gora Science Exhibition:**

Students of II & III B.Sc(M.E.Cs) attended Gora Science Exhibition organised at Gora Science centre on 05, 06, 07 DEC 2018.

